Attorney, J. I. J'maev Reg. No. 45,669 EO 901 223 145 US August 1, 2003

## **ABSTRACT**

Design information representing an electrical circuit is received and an electrical circuit condition requiring test is identified. A design verification test is determined, and evaluated for effectiveness in exercising the identified electrical circuit condition. Electrical circuit conditions include faults such as speed paths, races, coupling events, noise events, and voltage and temperature sensitivities.